

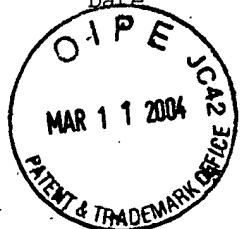
CERTIFICATE OF MAILING

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3/5/04

Date

Rebecca A. Baumann
Signature: Rebecca A. Baumann



PATENT

Attorney Docket No. SYN-0551CON1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application)	<u>PATENT APPLICATION</u>
)	
Inventor(s): John X. Zhong et al.)	
)	Art Unit: 2812
Application No.: 10/620,628)	
)	Examiner: Stacy Whitmore
Filed: 7/15/2003)	
)	
Title DESIGN VERIFICATION BY SYMBOLIC)	
SIMULATION USING A NATIVE)	
HARDWARE DESCRIPTION LANGUAGE)	
)	

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Listed below or on an attached Form PTO-1449 is information known to applicant(s). A copy of each listed publication and non-U.S. patent, is being submitted herewith, along with a concise explanation of information in a foreign language, if any, pursuant to 37 C.F.R. §1.97-1.98.

Applicants respectfully request that the listed information be considered by the Examiner and be made of record in the above-identified application. If form PTO-1449 is enclosed, the Examiner is requested to initial and return it in accordance with MPEP § 609.

This statement is not intended to represent that a search has been made or that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56.

- This statement qualifies under 37 C.F.R. § 1.97, subsection (b) because (check all that apply):
- (1) It is being filed within 3 months of the application filing date and is other than a continued prosecution application under § 1.53(d)
-- OR --
 - (2) It is being filed within 3 months of entry of a national stage
-- OR --
 - (3) It is being filed before the mail date of the first Office Action on the merits.
-- OR --
 - (4) It is being filed before the mailing of a first Office Action after the filing of a request for continued examination under § 1.114
- 37 C.F.R. § 1.97(c). If this statement is being filed after the period specified in § 1.97(b), but before the mailing date of the earlier of a final office action under § 1.113, a notice of allowance under § 1.311, or an action that otherwise closes prosecution in the application, then:
- a certification as specified in § 1.97(e) is provided below; **or**
 - a fee of \$180.00 as set forth in § 1.17(p) is authorized below, enclosed, or included with the payment of other papers filed together with this statement.
- 37 C.F.R. § 1.97(d). If this statement is being filed after the period specified in § 1.97(c), but on or before payment of the issue fee, then:
- A. a certification as specified in § 1.97(e) is completed below; **and**
 - B. a fee of \$180.00 as set forth in § 1.17(p) is authorized below, enclosed, or included with the payment of other papers filed together with this statement.
- Fee Authorization.* The Commissioner is hereby authorized to charge any additional fees or credit any overpayment associated with this communication to Deposit Account No. 50-0574 (Docket No. SYN-0551CON1).

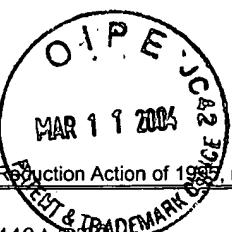
Respectfully submitted,

BEVER, HOFFMAN & HARMS, LLP

Dated: 3-5-04

By: 
Jeanette S. Harms, Reg. No. 35,537

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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT***(Use as many sheets as necessary)*

Sheet 1 of 1 Attorney Docket Number SYN-0551CON1

U.S. PATENT DOCUMENTS

Examiner Initials*	Cited No. ¹	Document Number Number - Kind Code ^{2(if known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Figures Appear
	A01	US-5,910,897	06/1999	Dangelo	
	A02	US-6,295,623	B1	Lesmeister	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document Cntry Code ³ Number ⁴ Kind Code ^{5(if known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Figures Appear	T ⁶

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Martinolle, F. et al. "A procedural language interface for VHDL and its typical applications," Jan. 1998, ACM, pp. 32-38
Gough, G. et al. "A design and verification environment for ELLA," Sep. 1995, IEEE, pp. 685-690
Wan, B. et al. "VHdbox: an X window system based high level debugger for the VHDL simulation environment," Oct. 1995, IEEE, pp. 358-360
Viklund, L. et al. "An object-oriented language for symbolic computation-applied to machine element analysis," 1992, ACM, pp. 397-405
Dawson, C. et al. "The verilog procedural interface for the verilog hardware description language," 1996, IEEE, pp. 17-22
Meyer, S. "Verilog plus c language modeling with PLI 2.0: the next generation simulation language," Mar. 1998, IEEE, pp. 98-105
York, G. "An integrated environment for HDL verification," Mar. 1995, IEEE, pp. 9-18

EXAMINER SIGNATURE:

DATE CONSIDERED:

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

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